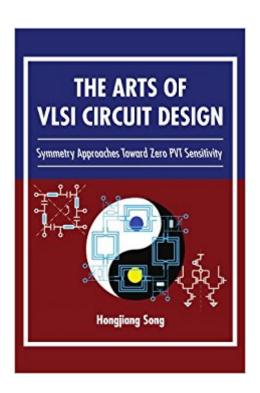
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# The Arts Of VLSI Circuit Design: Symmetry Approaches Toward Zero PVT Sensitivity





# Synopsis

This text is based on the class notes of a VLSI signal processing circuit course series (EEE598) the author developed for the EE department at Arizona State University. The materials are organized into nineteen special topics covering various state-of-the-arts symmetry based VLSI circuit design techniques for basic VLSI circuit elements, circuit modules and systems, where the symmetry principle and methods with inherently low PVT sensitivity are used to design VLSI circuits with superior scalability and performance for various VLSI SOC applications.

### **Book Information**

Paperback: 466 pages

Publisher: Xlibris, Corp. (March 2, 2011)

Language: English

ISBN-10: 1456874667

ISBN-13: 978-1456874667

Product Dimensions: 6 x 1 x 9 inches

Shipping Weight: 1.7 pounds (View shipping rates and policies)

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